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topics covered are as follows data structures multi mode multi corner analysis design constraints floorplan and timing placement and timing clock tree synthesis final route and timing design signoff rather than go into great technical depth the author emphasizes short clear descriptions which are implemented by references to authoritative manuscripts it is the goal of this book to capture the essence of physical design and timing analysis at each stage of the physical design and to show the reader that physical design and timing analysis engineering should be viewed as a single area of expertise this book is intended for anyone who is involved in asic design implementation starting from physical design to final design signoff target audiences for this book are practicing asic design implementation engineers and students undertaking advanced courses in asic design volume is indexed by thomson reuters cpci s wos the papers presented and reviewed in this volume are cover up to date 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